

## Encapsulation structure for display devices

### FIELD OF THE INVENTION

The present invention relates to encapsulation structures for display devices, methods for producing such structures, as well as to display devices comprising such encapsulation structures.

5

### BACKGROUND OF THE INVENTION

Display devices are known in graphic display and imaging art. Examples of such devices are liquid crystal displays (LCD) and electroluminiscent devices, such as LED (light emitting diode) based devices, organic light emitting diode (OLED) devices and 10 polymer light emitting diode (polyLED) devices.

An electroluminiscent device is a device comprising electroluminiscent material capable of emitting light when a current is passed through it, the current being supplied by means of electrodes. Electroluminiscent devices of the diode type preferentially pass current in one direction and generally comprise electroluminiscent material disposed 15 between an anode and a cathode.

Display devices are often sensitive to oxygen and moisture etc and, consequently, may degrade when exposed to the atmosphere. Exposure to oxygen and/or moisture, particularly in the presence of light, may lead to photo-oxidative degradation of a polymer material used.

20 Furthermore, oxidation at the cathode/polymer interface is one of the first problems arising in connection to oxygen and/or moisture diffusion in devices such as OLEDs or polyLEDs. Such reactions will significantly reduce the performance of the light emitting properties of the device.

Moreover, other materials used in the display device may also degrade when 25 exposed to the atmosphere. Therefore, there is a need for sealing the device.

Efforts have been made by evaporating metal films to seal display devices. However, these films often contain pinholes. Therefore, these films need to be relatively thick, which results in poor light transmission. Many of the known processes for achieving a more or less proof encapsulation of the devices require temperatures above 300°C during the

sealing process. Most polymer-based devices are not compatible with such high temperatures.

Furthermore, display devices, and in particular the actual diode structures of a polyLED/OLED display, are sensitive to mechanical forces applied to the devices. Scratching 5 the surface of a sealed display device could lead to the breaking of the surface. The surface at structures in the device, such as negative resists and other protrusions, are especially sensitive. Breaking of the surface would affect the display in the sense of loss of function in parts of the display device. It would also mean that a physical hole appear in the sealing of the surface, through which moisture and oxygen can permeate and reduce the lifetime of the 10 device. Thus there is a need for encapsulation of the sealed device.

US 6 413 645 discloses a barrier assembly comprising a barrier layer and a polymer layer formed on top of the barrier layer. The assembly may be applied on top of a display device with low temperature methods, thus covering and protecting the device from water and oxygen. The barrier assembly consists of at least one layer of a barrier material, 15 such as metal oxides, metal nitrides and metal carbides etc, that acts as water/oxygen barriers. The polymer layer has no intrinsic barrier function, but works as a planarisation layer.

However, the barrier assembly disclosed in US Pat 6 413 645 is vulnerable to mechanical forces. If a mechanical force is applied to the device, the barrier assembly would be damaged, and the water/oxygen permeability would be drastically impaired. In order to 20 strengthen the assembly, the deposition of additional layers, such as protective polymer layers and scratch resistant layers, on the barrier assembly is proposed. However, these extra layers introduces additional steps into the manufacturing method, which thus becomes complicated and expensive, and the total barrier assembly with the extra protective layers becomes relatively thick.

25

## SUMMARY OF INVENTION

An object of the present invention is to provide an encapsulation structure, which overcomes the above-described problems with the prior art.

This and other objects are achieved by an encapsulation structure for a display 30 device, comprising a dielectric sealing structure and a stabilisation layer. The dielectric sealing structure forms an essentially water/oxygen impermeable film on the device. The stabilisation layer is formed on the sealing structure and is intended to stabilise and protect the sealing structure, as well as to provide a scratch resistant surface on the display device.

In one embodiment, the sealing structure comprises at least two layers of dielectric material. By using at least two layers, a pinhole free structure is obtained. The sealing structure may be transparent and may comprise a third layer of a dielectric material formed on the top on the second layer.

5 In accordance with one embodiment of the invention, the first and the third layer of the sealing structure are made of the same dielectric material. However, the first and the third layer may also be made of different dielectric materials.

10 The dielectric material in the second layer is selected from the group comprising silicon oxide, silicon oxynitride, silicon oxide fluoride, titanium oxide, tantalum oxide, zirconium oxide, hafnium oxide, aluminium oxide, or any mixture thereof.

15 The stabilisation layer is thick enough to cover protruding structures in the sealing structure, so that said protruding structures are encapsulated in the material forming the stabilisation layer and covered under the essentially flat surface of said stabilisation layer. The stabilisation layer according to the present invention may comprise a cured (or otherwise hardened), e.g. thermally cured, material. Said material may be a polymeric or non-polymeric organic or inorganic material which may be transparent.

20 In accordance with one embodiment of the invention, the stabilisation layer is deposited on the device by inkjet printing. The stabilisation layer is hardened or cured subsequent of the deposition.

25 The display device may be a polyLED (polymer light emitting diode) or an OLED (organic light emitting diode) based display comprising protruding structures with negative slopes, such as negative resists.

The present invention also relates to a method for the manufacture of an encapsulation structure for a display device, comprising depositing a sealing structure on the device and depositing a stabilisation layer on top of the sealing structure.

The present invention also relates to a display device comprising an encapsulation structure in accordance with the invention.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter.

30

#### BRIEF DESCRIPTION OF THE DRAWING

The present invention will now be further described with reference to the accompanying drawing in which:

Figure 1 shows a schematic cross sectional view of one embodiment of a display device protected by an encapsulation structure in accordance with the present invention.

## 5 DESCRIPTION OF PREFERRED EMBODIMENTS

The present invention relates to an encapsulation structure for a display device, comprising a sealing structure 3 and a stabilisation layer 5. In a preferred embodiment of the invention, the sealing structure 3 comprises a first layer 6 of a first dielectric material and a second layer 7 of a second dielectric material. In one embodiment of the invention, said 10 sealing structure 3 also comprises a third layer 8 of a third dielectric material. Said third dielectric material may be of the same material as said first dielectric layer.

Figure 1 illustrates an embodiment of a polyLED display device arranged on a substrate 1. The device further comprises a polymer light emitting diode (polyLED) 2, and protrusions 4. The display device is sealed with a sealing structure 3, comprising three layers 15 of dielectric materials 6, 7, 8, and finally a stabilisation layer 5 which encapsulates the display device and stabilises the structures on the display device.

The substrate 1 may be a soda-lime or boron-silicate glass. However, other more or less oxygen and/or moisture permeable substrates may be used, such as metals, plastic and/or flexible substrates. Moreover, the substrate may be transparent or opaque. The 20 substrate may also contain active components e.g. thin film transistors and/or passive components e.g. diodes and capacitors. The polyLED 2 may be formed on the substrate using suitable manufacturing methods known by the man skilled in the art.

The sealing structure 3, which function as a diffusion barrier to oxygen and/or moisture, is formed on the display device, and hence a much longer durability of the display 25 device is achieved. The term "sealing structure" as used herein, refers to an essentially water and/or oxygen impermeable structure, preferably a thin film comprising one or more layers that provides a barrier against water and/or oxygen. The sealing structure is deposited on the display device to protect the same from water and/or oxygen which otherwise would impair the performance of the display device.

In one embodiment of the present invention, the sealing structure 3 comprises 30 three layers 6, 7, 8 of dielectric materials, and may be transparent. The first layer 6 of the sealing structure may be of low pinhole density, and may cover substantially the entire surface of cathode material. The first layer of dielectric material is formed after the deposition of the cathode material without exposing the display device to air, i.e. keeping the

device in an inert atmosphere. The subsequent layers of dielectric material may be formed using the same technique as for the first dielectric layer.

The first layer 6 may comprise silicon nitride, which closes most of the area of the display device, and only leaves a few pinholes open. However, these pinholes would not 5 be closed by making the silicon nitride layer thicker, since the chemical surface of the pinholes prevents sticking of the nitride. By depositing a second layer 7, e.g. an oxide, the surface of the first layer is modified, and the oxide layer will cover the pinholes.

However, the oxide layer does not provide a significant diffusion barrier to oxygen/moisture as silicon nitride does, and therefore a third layer 8, e.g. comprising a 10 material suitable for use as said first layer, is deposited on top of the second layer. The third layer sticks to the oxide layer, and thus an intact surface without pinholes and with good barrier properties is achieved.

The sealing structure 3 may comprise a N-O-N or O-N-O structure, where "N" means a dielectric nitride-layer, e.g. a layer of silicon nitride or aluminium nitride, and where 15 "O" means a dielectric oxide layer, selected from a layer of silicon oxide, silicon oxide fluoride, titanium oxide, tantalum oxide, zirconium oxide, hafnium oxide, aluminium oxide or any mixture thereof. Among these, silicon oxide has shown surprisingly good results.

For the formation of the layers comprised in the sealing structure, a low temperature plasma enhanced chemical vapour deposition (PECVD) method may be used. 20 However, other techniques known by the man skilled in the art may be used depending on the materials used.

The sealing structure of the present embodiment comprises about 200 nm of silicon nitride, about 300 nm of silicon oxide and again about 200 nm of silicon nitride, deposited at 80 °C. Examples of the N-O-N sealing structure according to the present 25 invention have been shown to have a water permeation rate of as low as  $1 \times 10^{-6}$  g/m<sup>2</sup>/day, which may be additionally improved by increasing the thickness of the materials or by adding extra layers, for example forming a N-O-N-O-N, O-N-O-N-O, N-O-N-O-N-O-N or a O-N-O-N-O-N-O sealing structure.

In another embodiment of the invention, the sealing structure also comprises a 30 gettering layer formed as a additional layer included in the sealing structure. This gettering layer functions as a built in moisture trap for moisture sensitive devices. Examples of suitable materials for the gettering layers are BaO and CaO.

The sealing structure may also comprise an organic polymer layer inbetween two layers of dielectric material. Other sealing structures demonstrating a satisfactory

water/oxygen impermeability may also be used, alone or in combination with the above mentioned sealing structure. Such other sealing structures may for example comprise metallic layers, ceramic layers, combinations of these materials alone or with other layers.

The stabilisation layer 5 may be formed of a curable or otherwise hardenable composition, deposited in its liquid phase and cured subsequent of the deposition to form a scratch resistant, preferably transparent, solid or amorphous surface. The material for the stabilisation layer may be an UV or thermally curable polymeric or non-polymeric material. It could also be a low temperature melting metallic compound, such as Indium.

By a non-polymeric material is meant a material without a carbon based backbone, e.g. a silicon based material. Non-limiting examples of polymeric materials comprises epoxy based and acrylate based materials, and non-limiting examples of non-polymeric materials comprises silica gel based materials and 3-glycidooxypropyltrimethoxysilane.

In one embodiment of the invention, the stabilisation layer 5 is formed from a thermally cured inorganic composition. Examples of well working materials are found in the following exemplifying examples. The material and the corresponding curing method is preferably carefully chosen to be compatible with the sealing structure 3 and the display device. For example, polyLED and OLED displays with transparent cathodes are often sensitive to UV-radiation since the electroluminiscent polyLED and OLED materials are very sensitive to UV-radiation. In these cases it is often suitable to choose a thermally curable material for the stabilisation layer.

One advantage with the deposition of a hard, scratch resistant stabilising layer on the display device is that extra protective cover plates, as used in prior art, is no longer crucial. This provides possibilities to produce thinner and lighter, but still durable, display devices.

The stabilisation layer 5 is deposited on the sealing structure 3, and is preferably thick enough to cover protruding structures 4, such as negative resists and particles that were located on the display device before the deposition of the sealing structure. Any dust particles on the device before the depositing of the sealing structure may, because of the thinness of the sealing structure, cause unexpected protrusions in the sealing structure. These protrusions are water/oxygen impermeable but may introduce mechanical instability. Any mechanical force applied on them, might cause them to break the surface, thereby opening a hole in the sealing structure, through which water and oxygen can permeate.

The thickness of the stabilisation layer 5 implies that such protrusions will be effectively encapsulated, and the mechanical rigidity of the layer stabilises the protrusions, making the display device much less sensitive to mechanical influence.

The stabilisation layer 5 may be thick enough to cover at least some protruding structures 4 of the display device. In some cases protrusions caused by particles on the display device, and other protrusions of similar size, are covered under the essentially planar surface of the stabilisation layer. Larger structures, with sizes at least as the negative resists, may still cause protrusions in the stabilisation layer.

In other cases, essentially all structures on the surface of the display device are covered under an essentially planar surface formed by the stabilisation layer. The stabilisation layer preferably forms an essentially planar surface on top of said protruding structures, and functions as a scratch protection layer that protects underlying structures to break the surface under influence of an external mechanical force. The thickness of the stabilisation layer, measured from a point on the surface of the sealing structure where no protrusion are located, is at least 0.1 µm, and may in some cases be up to 100 µm thick. The optimal thickness is different for different types of display devices with different structures on the surface.

For display devices which do not have said protruding structures, e.g. some active matrix displays, where there are no negative resists, the stabilisation layer is still required to stabilise enclosed particles. In that case the stabilisation layer should be at least 0.1 µm.

By the expression “negative resist” is meant a protruding section, the slope of which creates a shadow region. The negative resist may under some circumstances include a section having perpendicular side walls. In a polyLED display device, the negative resist has a function of patterning the upper electrode layer, i.e. the cathode, by providing a shadow region in which electrode material is not deposited when depositing the upper electrode layer. The negative resists are preferably made of an insulating material, and suitable materials include polymer-based photoresists, SiO<sub>2</sub>, Si<sub>3</sub>N<sub>4</sub> Al<sub>2</sub>O<sub>3</sub>. The breadth of the negative resists are e.g. in the range of 1 to 50 µm and the height of the negative resists are e.g. in the range of 0.3 to 10 µm.

The stabilisation layer also fills up the structures of the display device, forming an essentially cavity-free tight fitting encapsulation, forming an essentially cavity-free interface between the stabilisation layer and the display device and its sealing structure respectively. Cavities or bubbles in the stabilisation layer or in the interface between the

stabilisation layer and the display device may impair, not only the strength of the encapsulation, but also the optical properties of the display, in particular for so called top emissive displays, where the encapsulation structure faces the potential user. By “essentially cavity-free” is meant that there are no or very few cavities or bubbles in the material.

5 Further, the material in the stabilisation layer and the encapsulated sealing structure preferably have matching thermal expansion coefficients. With “matching” means that the differences in thermal expansion of the materials at the temperatures that the device is subjected to, does not cause any parts of the display device to break the surface.

10 The stabilisation layer may be opaque or transparent, depending on the type of display device. For top emissive displays, where the stabilisation layer faces the potential user, at least 70% transparency in the visible wavelengths are preferred. For down emitting displays, where the stabilisation layer is facing away from the potential user, transparency is not important.

15 The stabilisation layer may be deposited on the display device by several different methods. Such methods include, but are not limited to, spin coating with photolithography defining the structure, spray coating with a shadow mask, flexographic printing, micro-contact printing, screen printing, and inkjet printing.

20 In one embodiment of the invention, the stabilisation layer is deposited on the display device by inkjet printing of the material for the stabilisation layer in its liquid state. Compared to many of the other possible methods, the inkjet method provides low material consumption and good tolerance and quality (e.g. no bubbles) in the printing. Furthermore, no contact between the printing head and the display device is needed, which otherwise may damage the sealing structure. An example of the parameters for inkjet printing and subsequent processing for the manufacturing of the stabilisation layer is disclosed in the 25 following examples.

30 The display device on which an encapsulation structure in accordance with the present invention is deposited may be a liquid crystal display, or a light emitting diode based display, e.g. a polyLED (polymer light emitting diode) or a OLED (organic light emitting diode) based display. The display device may be an active or passive polyLED/OLED display comprising protrusions, such as negative resists, that creates shadow regions.

The present invention should not be considered as being limited to the above described embodiments and the following experiments. It includes all possible variations covered by the scope defined by the appended claims. The encapsulation structure may be

used in any situation where an scratch resistant oxygen and/or moisture barrier encapsulation is needed.

### Examples

5

#### Example 1: Organic planarisation layer

A UV-lacquer, SU-8, commercially available from MicroChem Corp, was deposited on a polyLED device with negative resists by several different depositing methods that all worked good. A N-O-N-O-N sealing structure was already deposited on the device.

- 10 The lacquer has a very low viscosity and therefore it fills the negative angles below the negative resists well. The lacquer was dried at temperatures around 95°C, and then the lacquer was exposed to UV-light (400 mJ at 365 nm) and thereby hardened. The stabilisation layer was transparent and gave a good planar surface on the device.

The different depositing methods used were: Spin Coating with  
15 photolithography defining the structure, spray coating with a shadow mask, screen printing, and inkjet printing. Different concentrations of the UV-lacquer was used depending on the different depositing methods.

#### Example 2: Inorganic planarisation layer

- 20 The following composition was mixed, refluxed for 2 hours and then cooled down to room temperature:

74 g 3-glycidooxypropyltrimethoxysilane

20 g methanol

1 g Aluminium di-s-butoxide ethylacetatoacetate

- 25 17 g HCl (0,005 N)

After deposition of a N-O-N-O-N sealing structure on polyLED device with negative resists, the above described composition was deposited on the device by several different depositing methods. The composition was dried at 80°C and further hardened at 120°C. The thickness of the stabilisation layer could be varied from 20 µm to at least 60 µm.

- 30 The stabilisation layer was transparent and gave a good planar surface on the device.

The depositing method used was spray coating with a shadow mask. Other possible depositing methods are screen printing and inkjet printing.

Example 3: Inkjet printing

The UV-lacquer SU-8 was diluted with  $\gamma$ -butyrolactone in order to obtain a proper viscosity for inkjet printing of the lacquer of about 10 cP. The experiments were performed with a single nozzle ink jet head. After deposition of a N-O-N-O-N sealing structure on polyLED device with negative resists a 15  $\mu\text{m}$  thick stabilisation layer was deposited on the device by inkjet printing of the solution in the lines formed between two adjacent parallel negative resists. 150 pl drops were printed at a distance of 12  $\mu\text{m}$  with a table speed of 15mm/s. Every printed line merged with an adjacent printed line to form a ~50  $\mu\text{m}$  thick continuous layer covering the negative resists. The lacquer was dried at temperatures around 55 °C, and then the lacquer was exposed to UV-light (400 mJ at 365 nm) and thereby hardened to form a 15  $\mu\text{m}$  thick stabilisation layer. This printing method is easily transferred to a multi nozzle inkjet system for quicker printing of larger areas.

To prevent the printed (not hardened) liquid from spreading outside the edges of the display device during this experiment, it was found favourable to arrange protruding barriers at the ends of the lines formed by the space between two parallel adjacent negative resists, which lines were used for the deposition of the lacquer. The barriers were arranged at each end of said lines, inbetween the ends of two parallel adjacent negative resists. The purpose of the protruding barriers were to close the opening between the ends of said parallel adjacent negative resists, to effectively stop the capillary transport of liquid lacquer from the display device. The use of such protruding barriers provided a minimal leakage of the printed liquid lacquer out from the display device, and resulted in a planar surface of the stabilisation layer, even at the edges of the display device.